

• General Description

It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

• Features

- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

- BLDC Motor driver
- DC-DC
- Battery protection

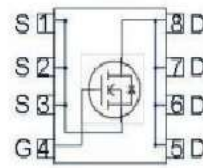
• Ordering Information:

Part NO.	ZMS008N04HNC
Marking	ZMS008N04H
Packing Information	REEL TAPE
Basic ordering unit (pcs)	3000

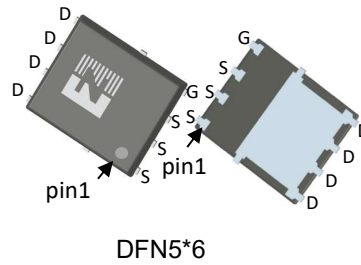
• Absolute Maximum Ratings ($T_C=25^{\circ}C$)

Parameter	Symbol	Conditions	Value	Unit
Drain-Source Voltage	V_{DS}	$25^{\circ}C \leq T_j \leq 175^{\circ}C$	40	V
Gate-Source Voltage ^①	V_{GS}		± 20	V
Continuous Drain Current	I_D	$T_C=25^{\circ}C$	310	A
	I_D	$T_C=75^{\circ}C$	247	A
	I_D	$T_C=100^{\circ}C$	214	A
Pulsed Drain Current ^①	I_{DM}	Pulsed; $t_p \leq 10 \mu s$; $T_{mb} = 25^{\circ}C$;	930	A
Total Power Dissipation	P_D	$T_C=25^{\circ}C$	167	W
Total Power Dissipation	P_D	$T_A=25^{\circ}C$	4.2	W
Operating Junction Temperature	T_J		-55 to +175	$^{\circ}C$
Storage Temperature	T_{STG}		-55 to +175	$^{\circ}C$
Single Pulse Avalanche Energy	E_{AS}	L=0.1mH, VGS=10V, Rg=25 Ω ,	320	mJ
		L=0.5mH, VGS=10V, Rg=25 Ω ,	680	mJ
ESD Level (HBM)			CLASS 2	

• Product Summary



$V_{DS} = 40V$
 $R_{DS(ON)} = 0.7m\Omega$
 $I_D = 310A$



DFN5*6



•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	RthJC		-	0.9	°C/W
Thermal resistance, junction-ambient	RthJA ^②		-	36	°C/W
Soldering temperature	Tsold		-	260	°C

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	40			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	2	2.7	4	V
Drain-Source Leakage Current	I _{DSS}	V _{GS} =0V, V _{DS} = 40V			1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} = 0V			100	nA
Static Drain-source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D = 40A		0.7	0.91	mΩ
Forward Transconductance	g _{FS}	V _{GS} =5V, I _{SD} = 10A		30		s
Diode Forward Voltage	V _{FSD}	V _{GS} =0V, I _{SD} = 40A			1.3	V

•Dynamic characteristics

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	Ciss	f = 1MHz, V _{DS} =25V	-	5430	-	pF
Output capacitance	Coss		-	1520	-	
Reverse transfer capacitance	Crss		-	84	-	
Gate Resistance	Rg	f = 1MHz	-	1.6		Ω
Total gate charge	Qg	V _{DD} = 15V, I _D = 20A, V _{GS} = 10V	-	87	-	nC
Gate - Source charge	Qgs		-	21	-	
Gate - Drain charge	Qgd		-	19	-	
Turn-ON Delay time	t _{D(on)}	V _{GS} =10V, V _{DS} =15V, R _G =3.3Ω, I _D =20A	-	15	-	ns
Turn-ON Rise time	t _r		-	10	-	ns
Turn-Off Delay time	t _{D(off)}		-	26	-	ns
Turn-Off Fall time	t _f		-	17	-	ns
Reverse Recovery Time	t _{RR}		V _{DD} =20V, dI _S /dt = 100A/us, I _S =50A	-	65	-
Reverse Recovery Charge	Q _{RR}		-	95	-	nC

Fig.1 Gate-Charge Characteristics

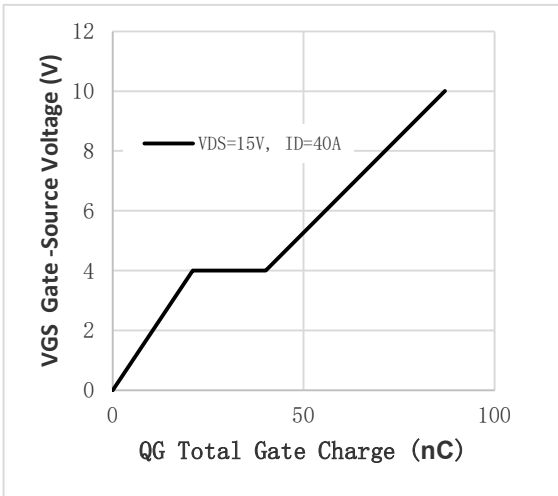


Fig.2 Capacitance Characteristics

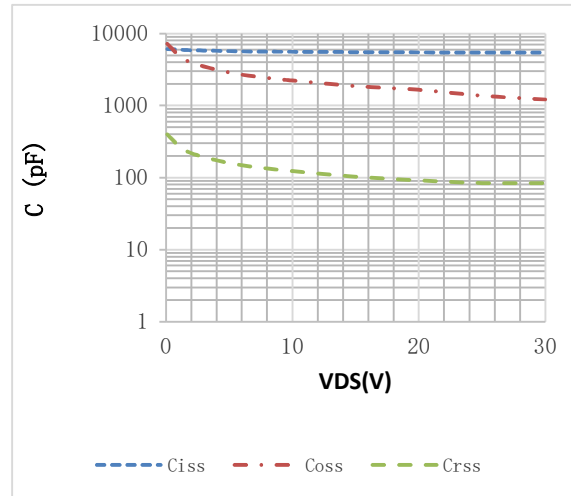


Fig.3 Power Dissipation

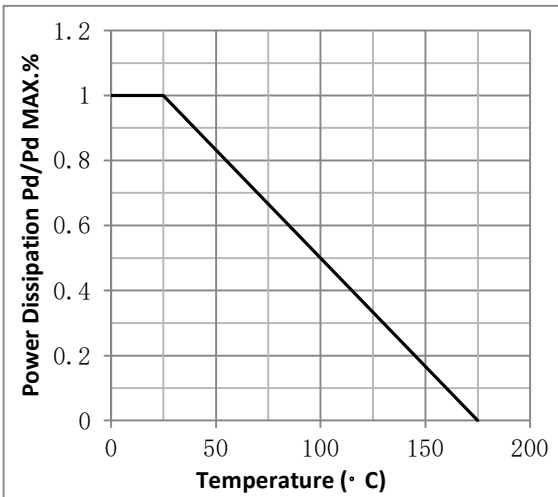


Fig.4 Typical output Characteristics

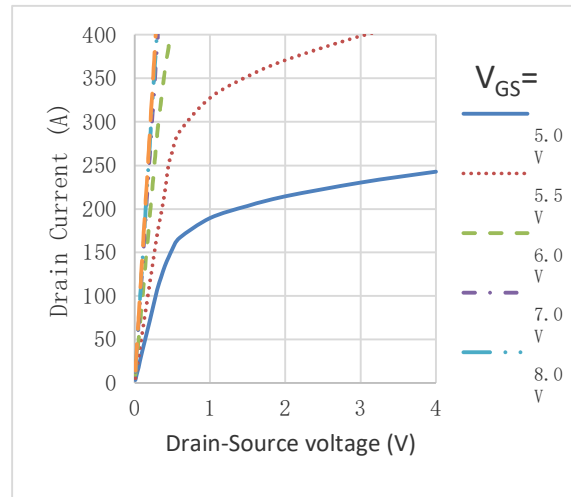


Fig.5 Threshold Voltage V.S Junction Temperature

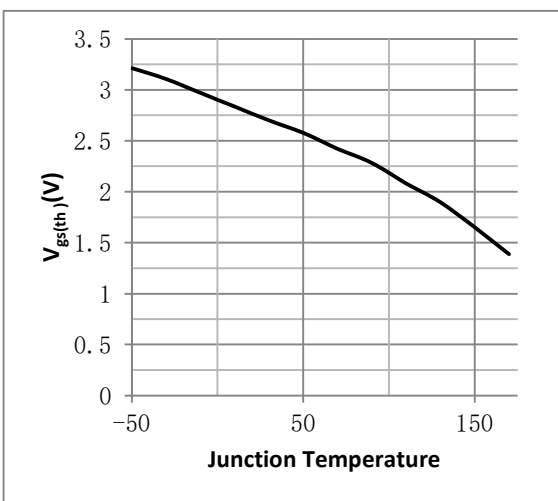


Fig.6 Resistance V.S Drain Current

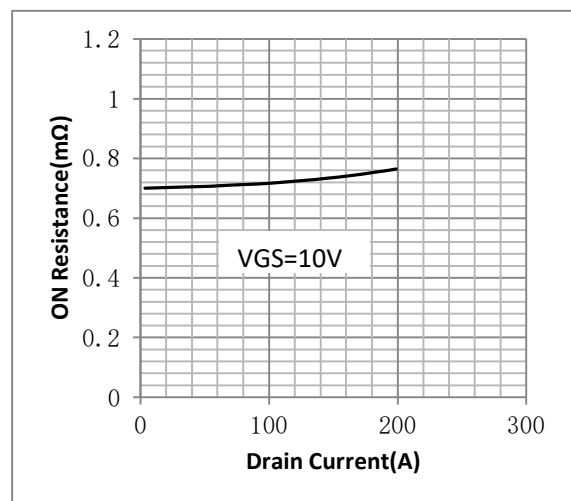


Fig.7 On-Resistance VS Gate Source Voltage

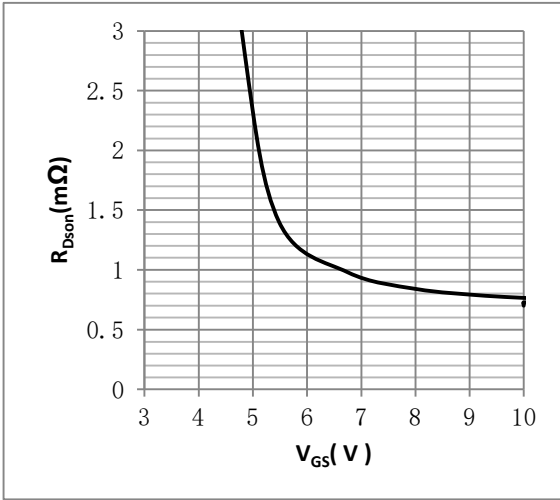


Fig.8 On-Resistance V.S Junction Temperature

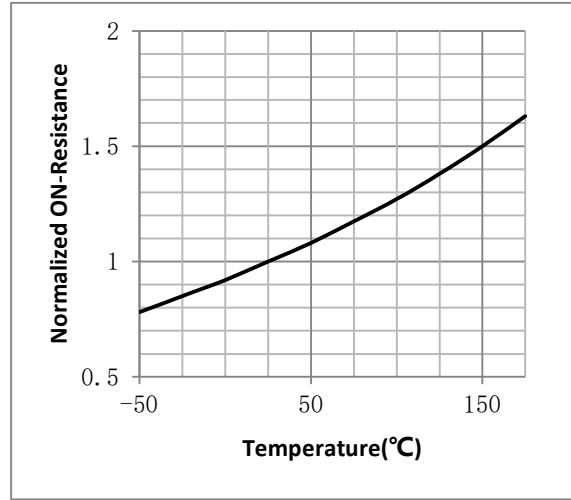


Figure 9. Diode Forward Voltage vs. Current

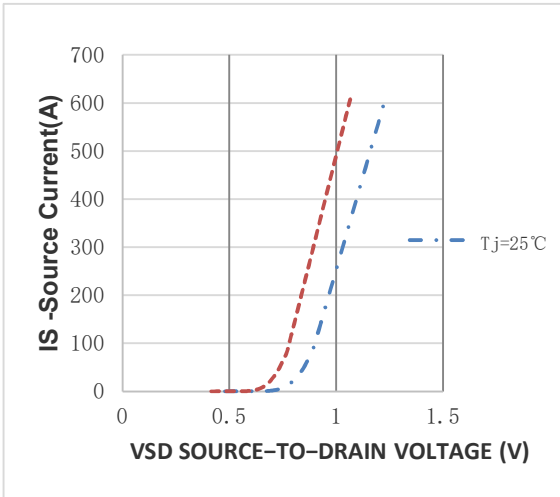


Figure 10. Transfer Characteristics

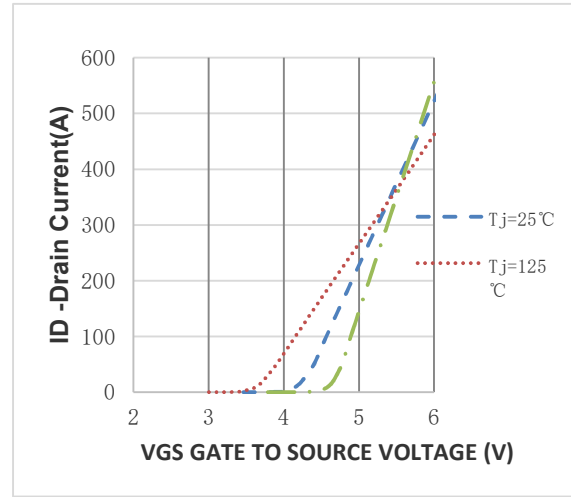


Fig.11 SOA Maximum Safe Operating Area

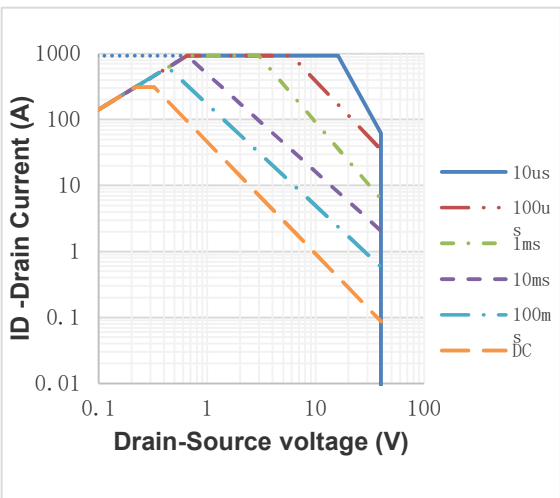
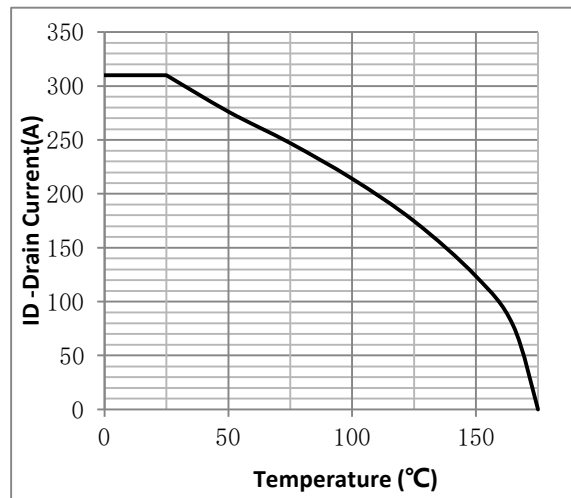
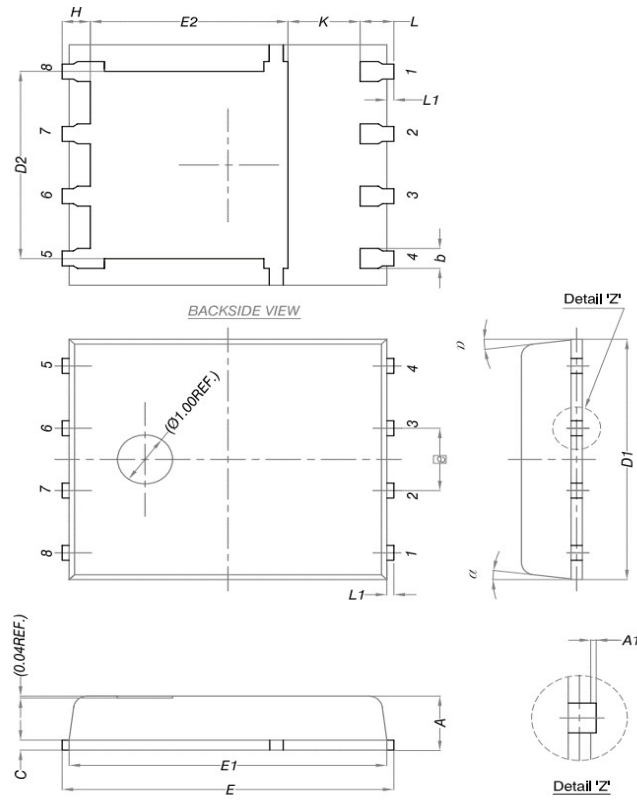


Fig.12 I_D vs. Junction Temperature^③



•DFN5*6 Package Outline



Note:

- ① Pulse : VGS=+20V/-20V, Duty cycle=50%, Tj=175°C, t=1000 hours; For DC , the following test conditions can be passed: VGS=+20V/-10V, Tj=175°C, t=1000 hours;
- ② Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;
- ③ Practically the current will be limited by PCB, thermal design and operating temperature.
VGS=10V.

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Revision History

Version	Date	Change
A	2022.6.15	New
B	2022.9.5	1.Add Reach, HF figure 2.ID curve modify